

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

| APPL NUM | FILING DATE | CLASS | SUBCLASS | GAU | EXAMINER |
|----------|-------------|-------|----------|------|----------|
| 10080884 | 02/22/2002 | 438 | 704 | 1765 | Des |

****APPLICANTS:** Kim Do-Hyung; Kim Sung-Bong; Hong Jung-In;

****CONTINUING DATA VERIFIED:**
gl

**** FOREIGN APPLICATIONS VERIFIED:**
 REPUBLIC OF KOREA 2001-09381 02/23/2001
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|---|---|----------------------------------|
| PG-PUB <input type="checkbox"/> | DO NOT PUBLISH <input type="checkbox"/> | RESCIND <input type="checkbox"/> |
| Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no | ATTORNEY DOCKET NO | |
| 35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no | 4591-226 | |
| Verified and Acknowledged Examiners's initials <i>gl</i> | | |
| TITLE : Method of forming a device isolation trench in an integrated circuit device | | |

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

| NOTICE OF ALLOWANCE MAILED | | CLAIMS ALLOWED | |
|--|--|---|----------------------|
| ISSUE FEE Amount Due Date Paid | | Total Claims | Print Claim for O.G. |
| | | DRAWING Sheets Drwg. Figs. Drwg. Print Fig. | |
| <input type="checkbox"/> TERMINAL DISCLAIMER | | Application Examiner | |
| | | PREPARED FOR ISSUE | |
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